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Wriddhi Chakraborty received the Best Student Paper Award for Technology at the 2020 VLSI Symposia on Technology and Circuits for his paper titled "BEOL Compatible Dual-Gate Ultra Thin-Body W-Doped Indium-Oxide Transistor with $I_{on}=370$ uA/um, $SS=73$ mV/dec and $I_{on/I_{off}}>4E9$ ". A third-year Ph.D. student, Chakraborty is a member of the Nanoelectronic Devices and Circuits Lab.

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Basic Electrical and Electronics Engineering is a common subject for first-year students who have chosen their branch as ECE, CEC, Civil, Mechanical, and more (except BT). This subject provides an exceptional appearance to the entire extent of topics like Electricity Fundamentals, Network Theory, Electro-magnetism, Electrical Machines, Transformers, Measuring Instruments, Power Systems, Semiconductor Devices, Digital Electronics, and Integrated Circuits.

Circuit Theory And Network Analysis By Chakraborty

Chakraborty Network topology is a graphical representation of electric circuits. It is useful for analyzing complex electric circuits by converting them into network graphs. Network topology is also called as Graph theory. Basic Terminology of Network Topology. Now, let us discuss about the basic terminology involved in this network topology. Graph Page 12/28

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